VITA

February 15, 1964	Born in Ann Arbor, MI
1986	B.S. Computer Science and Engineering Massachusetts Institute of Technology Cambridge, MA
1986-1987	Chancellor's Fellow University of California Los Angeles, CA
1987-1990	Research Assistant, Research Associate University of California Los Angeles, CA
1991	M.S. Computer Science University of California Los Angeles, CA
1991-1993	NASA/DARPA Research Assistantship in Parallel Processing
1993-1994	Teaching Associate University of California Los Angeles, CA
1994-	Senior Software Engineer SAIC McLean, Virginia

PUBLICATIONS AND PRESENTATIONS

1. G. L. Frazier and Y. Tamir, ‘‘The Design and Implementation of a Multi-Queue Buffer for VLSI Communication Switches,’’ *International Conference on Computer Design*, Cambridge, MA, pp. 466-471 (October, 1989).

2. Y. Tamir and G. L. Frazier, ‘‘Dynamically-Allocated Multi-Queue Buffers for VLSI Communication Switches,’’ *IEEE Transactions on Computers* **41**(6), pp. 725-737 (June 1992).

3. Y. Tamir and G. L. Frazier, ‘‘Hardware Support for High-Priority Traffic in VLSI Communication Switches,’’ *Journal of Parallel and Distributed Computing* **14**(4), pp. 402-416 (April 1992).

4. Y. Tamir and G. L. Frazier, ‘‘Support for High-Priority Traffic in VLSI Communication Switches,’’ *9th Real-Time Systems Symposium*, Huntsville, AL, pp. 191-200 (December 1988).

5. Y. Tamir and G. L. Frazier, ‘‘High-Performance Multi-Queue Buffers for VLSI Communication Switches,’’ *15th Annual International Symposium on Computer Architecture*, Honolulu, Hawaii, pp. 343-354 (May 1988).