IMPROVING THE QUADRATIC OBJECTIVE FUNCTION IN MODULE
PLACEMENT

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Abstract

Traditional goals in placement for cell-based designs involve minimizing either total wire-length or channel density; each of these metrics in some sense corresponds to final layout area. In traditional approaches, a net model is used to transform the netlist hypergraph into a graph representation. A *linear* wirelength objective is then derived, but then for technical reasons a *quadratic* form is actually optimized using relaxation or eigenvector methods. We address this seeming inconsistency and propose a simple transformation of the quadratic objective which "recaptures" the original minimum-wirelength objective. Computational results for a wide range of standard benchmarks show that this refinement gives very significant savings in both total wirelength and channel width for linear placement: these values are respectively reduced by an *average* of 7% and 18% over results obtained with the previous standard approach.

1 Preliminaries

Module placement has presented one of the most persistent challenges in IC layout synthesis. The true criterion for a good placement is efficient autoroutability, subject to performance and chip area constraints. Thus, placement models and objective functions are very hard to formulate. Many methods have been proposed, such as clustering, iterative improvement, row-assignment, recursive min-cut, force-directed, etc.; early techniques are surveyed by Hanan et al. [12], and Lengauer [15] gives a recent overview.

We may view the placement procedure as the assignment of n modules to n available slots such that a given cost function is minimized, i.e., we have a *quadratic assignment* formulation. In particular, to simplify our discussion we will address the problem of *linear placement*, where the modules are assigned to positions $1, \ldots, n$ on the one-dimensional integer lattice. The linear place-

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ment problem, which we will formalize below, captures a number of practical applications, e.g., placement within cell rows. Furthermore, as shown by Kurdahi and Sastry [14], two-dimensional placement in cell-based designs can be modeled as two interacting one-dimensional placement processes.

There are three main components to any module placement approach:

- First, a *net model* represents signal nets (hyperedges in the netlist hypergraph) by graph edges.

- Second, given the net model, an *objective function* is formulated for global minimization.

- Third, a (heuristic) *optimization method* must be used to minimize the objective function.

The results in this paper stem from the following simple observation: while standard net models and objective functions are aimed at minimizing wirelength, the accompanying optimizations actually minimize a completely different objective function which is *quadratic* in wirelength. Put another way, the traditional approach to placement algorithms becomes problematic in that one picks a net model which reflects a minimum wirelength objective, but then takes a "left turn" and instead minimizes the sum of squared wirelengths.

This inconsistency between the objective function developed from the net model and the cost function actually optimized is the subject of the present work. Our goal here is to determine an appropriate net model and objective function, and then be able to actually minimize a good approximation to this objective function. For a number of reasons, it appears that the linear wire-length objective is in fact a good objective function. And while technical reasons have historically motivated the substitution of the quadratic for the linear wirelength objective, we show that a simple change of weights in the quadratic form of the objective will "recapture" the effect of the original minimum-wirelength placement objective. Experimental results confirm that when our refinement is applied to traditional placement methods, very significant improvements result for both the total wirelength and maximum channel width metrics.

2

The remainder of this Extended Summary is organized as follows. In Section 2, we review the traditional placement approaches, i.e., net models, objective functions and optimization methods. This brings to light the above-mentioned "inconsistency", which we heuristically resolve via a method proposed in Section 3. Section 4 presents experimental results and conclusions.

2 The Placement Process

Placement has traditionally been formulated as a quadratic assignment problem where modules must be placed into available slots. The slots are often assumed to be linearly ordered, e.g., for backplane formation or cell placement within assigned rows. As noted above, this is reasonably general since two-dimensional placement may be described by two simultaneous one-dimensional placement processes [14].

Linear Placement: Given a netlist graph $G = (V, E)$ with $|V| = n$ and connection weights c_{ij} between module pairs (v_i, v_j), map the modules onto distinct positions x_i taken from the set $\{1, \ldots, n\}$ such that an objective function dependent on the c_{ij} and the module positions is minimized.

2.1 Net Models

In the first phase of the placement algorithm, a net model is derived which yields a graph representation of the netlist hypergraph; the net model reflects the routing cost of the placed netlist and determines the c_{ij} values in the above formulation. Many net models have been proposed, including spanning paths, spanning cycles, spanning trees, star topologies, etc. Several models can suffer from nondeterministic asymmetry in the connection weights c_{ij}, i.e., not all adjacencies derived from a given k-pin net will be accorded the same significance. Furthermore, minimum spanning tree, centroid-based star (e.g., [18]), or other topologies are inherently dynamic, requiring recomputation with every change in the module placement (see [15] for a survey).

The most common net model is that of a weighted clique, where a k-pin net will induce $C(k, 2)$

3

edges among its k modules (see Figure 1). The early survey by Hanan et al. [12] details several clique weighting variants which propose uniform weighting of the $C(k, 2)$ edges by such values as $\frac{2}{k}$, $\frac{1}{k}$, $\frac{1}{k-1}$, etc. Simple dimensional analysis shows that all of these net models are essentially identical in practice. Recent work has widely adopted a "standard" weighted clique model [15], wherein a k-pin net contributes $\frac{1}{k-1}$ to each of $C(k, 2)$ c_{ij} values. While the most obvious advantages of the clique model stem from its symmetry, we briefly digress here to provide additional intuition behind the standard edge weighting.
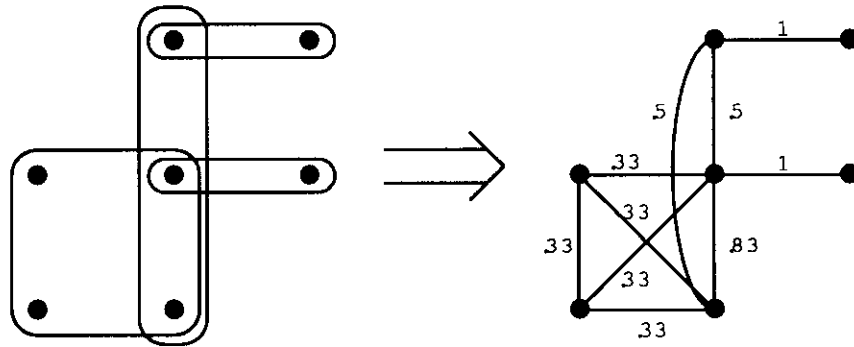

Figure 1: The standard clique net model: each k-pin net contributes $\frac{1}{k-1}$ to the c_{ij} value for each of the $C(k, 2)$ pairs of nodes (v_i, v_j) which belong to the net.

Since the goal of linear placement is to minimize the wire length contributed by each net, i.e., the net span in one dimension, we would like the weighting of each net to be related to the minimum possible wire length WL_{min} for each net. Intuitively, the weighting should be inversely proportional to WL_{min} so that the objective function does not "try too hard" to place the k modules of a net into $< k$ slots when such an arrangement is physically impossible.

In the discrete linear placement problem defined above, the minimum wirelength WL_{min} of a k-node net is $k - 1$, and by the above intuition we should adopt a weighting function on the order of $\frac{1}{k-1}$. Thus, our heuristic picture of linear placement supports use of the present standard clique net model.

4

2.2 Objective Function

Certainly, the goal of module placement must encompass minimization of total wirelength. Each grid unit of wire requires an additional quantum of chip area which is dependent on the wiring pitch. Moreover, larger wirelength generally implies larger RC constants, which can adversely impact system performance. Of course, layout area in cell-based designs is also determined by the number of wiring tracks used, i.e., the sum of channel widths is also an important objective function. Although minimum wirelength placement and minimum channel-width placement are not perfectly correlated, we note that results of Adolphson and Hu [1] may be used to establish a probabilistic relationship between the two metrics. Indeed, our experimental results below show that when we use our new methods to improve wirelength, the channel width usually also improves. In view of these arguments, we choose to minimize a sum of wirelengths objective function; for linear placement, this corresponds to minimizing the sum of net spans. Such a conclusion is in agreement with researchers who have considered the tradeoffs between linear and quadratic objectives. For example, Sigl et al. [18] recently concluded that "a linear objective function seems to reflect the actual wiring demands more accurately than the quadratic objective function".

2.3 Optimization Methods

Finally, it is well known that the Linear Placement formulation above is NP-complete when we minimize the objective function $\sum_{i,j} c_{ij}|x_i - x_j|$ [8]. By contrast, if we relax the slot constraint and introduce a quadratic form in the minimization, efficient numerical algorithms can be used to obtain a global optimum solution. More specifically, we represent the circuit netlist by the simple undirected graph $G = (V, E)$ with $|V| = n$ vertices $v_1, \ldots, v_n$. We use the $n \times n$ *connection matrix* $C = C(G)$, where $c_{vw} = 1$ if $(v, w) \in E$ and $c_{vw} = 0$ otherwise. If G has weighted edges, then c_{vw} is equal to the weight of $(v, w) \in E$, and by convention $c_{vv} = 0$ for all $v \in V$. If we let $d(v)$ denote the degree of node v (i.e., the sum of the weights of all edges incident to v), we get the $n \times n$ *diagonal matrix* D defined by $D_{ii} = d(v_i)$. As noted by, e.g., Hall [11], Cheng and Kuh [4], and Tsay et al. [20], the real eigenvector corresponding to the second smallest eigenvalue of Q gives

a linear placement solution vector $\vec{x}$ which minimizes $\sum_{i,j} c_{ij}|x_i - x_j|^2$ subject to the constraint $|\vec{x}| = 1.$ [1]

Eigenvector solutions to the linear placement problem with quadratic objective function can then be found by either iterative relaxation methods [4] [20] [18] or sparse-matrix operator techniques such as the Lanczos method [10].[2]

Some heuristic justifications for the squared wirelength objective have been put forth, notably that the metric reduces congestion since it tends to reduce the maximum wirelength of any net. However, it is by no means clear why so much effort has gone into optimizing the particular squared wirelength objective $\sum_{i,j} c_{ij}|x_i - x_j|^2$, which is not very reflective of the "true" cost function, $\sum_{i,j} c_{ij}|x_i - x_j|$. Indeed, much work within the past five years simply points to the formulations used by Hall [11] and Cheng and Kuh [4], rather than presenting any rationale for the quadratic objective. As Lengauer notes in [15] (p. 317): "The main reason why quadratic wire-length estimation is so popular is that the method leads to quadratic cost functions that can be minimized easily .... Thus, the final motivation for using quadratic wire length is driven by methodical, not modeling, arguments."

It is interesting to note that very few researchers besides Lengauer have commented on this issue. Sigl et al. [18] claim to be the first to explicitly address the differences between the linear

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[1]Hall [11] noted that the eigenvectors of the matrix $Q = D - C$ solve the problem of finding the vector $\underline{x} = (x_1, x_2, \ldots, x_n)$ minimizing

$$z = \frac{1}{2} \sum_{i=1}^{n} \sum_{j=1}^{n} c_{ij}(x_i - x_j)^2$$

subject to the constraint $|\underline{x}| = (\underline{x}^T \underline{x})^{1/2} = 1$. Since $z = \underline{x}^T Q \underline{x}$, to minimize z we may form the Lagrangian

$$L = \underline{x}^T Q \underline{x} - \lambda(\underline{x}^T \underline{x} - 1).$$

Setting the first partial of L with respect to $\underline{x}$ equal to zero yields

$$2Q\underline{x} - 2\lambda\underline{x} = 0,$$

which can be rewritten as

$$(Q - \lambda I)\underline{x} = 0$$

where I is the identity matrix. This gives an eigenvalue formulation for λ, and the eigenvectors of Q are the only nontrivial solutions for $\underline{x}$. The minimum eigenvalue 0 gives the uninteresting solution $\underline{x} = (1/\sqrt{n}, 1/\sqrt{n}, \ldots, 1/\sqrt{n})$, and hence the eigenvector corresponding to the second smallest eigenvalue λ is used. Again, note that the slot constraint is replaced by the requirement that $|x| = 1$.

[2]Using the eigenvectors of the second- *and* third-smallest eigenvalues of Q yields a two-dimensional placement which also minimizes the weighted sum of squared wirelengths.

and quadratic objectives. As noted above, they find the linear objective to be more "accurate". However, in order to minimize the linear objective via a quadratic formulation, Sigl et al. use a dynamic net model which requires an iterative algorithm. Thus, the method in [18] alternately solves a quadratic program and updates the coefficients of the program until a convergence criterion is satisfied. By contrast, the next section proposes a heuristic which empirically allows direct computation of a good linear placement via, e.g., a Lanczos sparse-matrix code [9] [10].

3 A Heuristic Improvement to the Quadratic Objective Function

In this section, we briefly outline intuition leading to an enhancement of the traditional quadratic objective for linear placement. Recall from the discussion of Section 2 that we would like to minimize $z = \sum_{i,j} c_{ij}|x_i - x_j|$, i.e., the weighted sum of wirelengths. However, due to complexity reasons we prefer a quadratic objective function which is amenable to global optimization.

To minimize z, we could minimize the *square* of z if we are restricted to using a quadratic objective function. However, the expansion of z^2 is the highly complicated expression

$$c_{12}^2|x_1 - x_2|^2 + c_{13}^2|x_1 - x_3|^2 + \cdots + 2 \cdot c_{12}c_{13}|x_1 - x_2||x_1 - x_3| + \cdots$$

which can be rewritten as

$$z^2 = \sum_{i=1}^{n} \sum_{j=1}^{n} c_{ij}^2|x_i - x_j|^2 + \Phi \quad (\equiv mixed\ terms)$$

Ignoring the sum of mixed terms Φ in this expression for z^2 leads us to a more natural quadratic minimization objective for linear placement:

$$z' = \sum_{i=1}^{n} \sum_{j=1}^{n} c_{ij}{}^2|x_i - x_j|^2$$

and we suspect that minimizing z' can be shown to be nearly equivalent to minimizing z^2, i.e.,

minimizing z, for a large class of problems. Certainly, the experimental results in the next section demonstrate that this simple modification leads to significant wirelength and channel-width savings.

4   Experimental Results and Conclusions

Given the weighted clique model, we solve the associated linear placement problem using the standard eigenvector-based approach outlined in Section 2 above, using a Lanczos-based implementation reported in [10]. Given a graph representation G for the netlist, the eigenvector corresponding to the second-smallest eigenvalue of $Q(G) = D(G) - A(G)$ gives a linear ordering of the modules. We simply evaluate the sum of net spans (i.e., total wirelength) and the channel width induced by this linear ordering.

Tables 1 and 2 show results for linear placement of a number of MCNC benchmarks (the Primary and Test suites), as well as for additional industry netlists (three ILLIAC boards and two benchmarks obtained from Hughes Aircraft Co.) that were evaluated in [21] and [10]. Using the standard clique net model, where each k-pin net containing modules v_i and v_j contributes $\frac{1}{k-1}$ to the value c_{ij}, we obtain results that reflect the methods of [20], [4] and a number of recent researchers. However, when we square the final c_{ij} values and apply the same quadratic optimization techniques, we reduce both wirelength and maximum channel width by an average of 7% and 18%, respectively. Since each of these metrics is representative of overall layout area, these improvements have a great deal of practical significance.

A number of interesting open issues remain. For example, it is possible that alternate functions of the c_{ij} (i.e., other than $c_{ij}{}^2$) can be applied to transform the standard quadratic objective.[3] As long as fixed c_{ij} are used, the usual efficient quadratic optimization algorithms remain applicable. In fact, it is quite reasonable to envision a placement methodology which tests a number of alternate functions of the c_{ij}, then returns the best result.

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[3]It is interesting to note that early work, e.g., that of Steinberg [19], proposed changing the exponent of the $|x_i - x_j|$ term in the objective function. However, no work has examined any alternate exponents for the c_{ij}.

Test problem	Number of elements	c_{ij}		c_{ij}^2		Percent WL reduction
		WL	squared WL	WL	squared WL	
IC67	67	2311	56943	2204	63320	4.63
IC116	115	4561	108187	4790	122420	-5.02
IC151	151	6809	188125	6351	224757	6.73
bml	882	56739	9006325	52822	9802464	6.90
19ks	2844	569362	325582810	403835	264652195	29.07
Prim1	833	57991	7998623	52267	9244365	9.87
Prim2	3014	1029741	659759635	803172	603056922	22.00
Test02	1663	316837	144173965	377974	235816852	-19.30
Test03	1607	168994	33363650	130070	48833696	23.03
Test04	1515	175023	42366645	225495	94964351	-28.84
Test05	2595	573500	317395208	563515	441759801	1.74
Test06	1752	435829	219226747	294939	201103961	32.33

Table 1: Results for twelve industry benchmarks showing wirelength and squared wirelength values when $\sum_{i,j} c_{ij}|x_i - x_j|^2$ and $\sum_{i,j} c_{ij}^2|x_i - x_j|^2$ objective functions are used with an eigenvector method to yield the linear placement. Average WL improvement is 7%.

Test problem	Number of elements	c_{ij}		c_{ij}^2		Percent Channel Width reduction
		Channel Width	Avg Density	Channel Width	Avg Density	
IC67	67	56	34.493	54	32.896	3.57
IC116	115	71	39.661	64	41.652	9.86
IC151	151	83	45.093	76	42.060	8.43
bml	882	128	64.330	114	59.889	10.94
19ks	2844	470	200.198	299	141.995	36.38
Prim1	833	144	69.617	121	62.745	15.97
Prim2	3014	583	341.653	382	266.480	34.48
Test02	1663	364	190.521	389	227.284	-6.87
Test03	1607	166	105.161	130	80.940	21.69
Test04	1515	213	115.527	237	148.842	11.27
Test05	2595	458	221.002	354	217.154	22.71
Test06	1752	518	248.761	252	168.344	51.35

Table 2: Results for twelve industry benchmarks showing channel width and average channel density values when $\sum_{i,j} c_{ij}|x_i - x_j|^2$ and $\sum_{i,j} c_{ij}^2|x_i - x_j|^2$ objective functions are used with the eigenvector method to yield the linear placement. Average reduction in channel width is 18%. As with the wirelength results, note that the gains seem larger as problem size increases.

In conclusion, we have retraced the traditional attack on linear placement of modules in cell-based layout. The basic observation is that a quadratic objective amenable to global minimization is usually substituted for the true linear wirelength objective; however, the standard quadratic form does not have a strong relationship to the original objective. Thus, we propose a simple modification of the weighting function used in the quadratic form of the objective. This allows global minimization of a function that more closely reflects the minimum wirelength goal. Indeed, experimental results confirm very significant savings in both wirelength and channel width. Since these metrics reflect layout area, we believe that our proposed modification will be highly useful

within the context of existing placement methods.

References

[1] D. Adolphson and T. C. Hu, "Optimal Linear Ordering", *SIAM J. Appl. Math.* 25 (1973), pp. 403-423.

[2] K. J. Antreich, F. M. Johannes and F. H. Kirsch, "A New Approach for Solving the Placement Problem Using Force Models", *Proc. IEEE Intl. Symp. on Circuits and Systems*, 1982, pp. 481-486.

[3] E. R. Barnes, "An Algorithm for Partitioning the Nodes of a Graph", *SIAM J. Alg. Disc. Meth.* 3(4) (1982), pp. 541-550.

[4] C. K. Cheng and E. S. Kuh, "Module Placement Based on Resistive Network Optimization", *IEEE Trans. on CAD* (1984), pp. 218-225.

[5] C. K. Cheng, "Linear Placement Algorithms and Applications to VLSI Design", *Networks* 17 (1987), pp. 439-464.

[6] W.E. Donath, "Logic Partitioning", in *Physical Design Automation of VLSI Systems*, B. Preas and M. Lorenzetti, eds., Benjamin/Cummings, 1988, pp. 65-86.

[7] J. Frankle and R.M. Karp, "Circuit Placement and Cost Bounds by Eigenvector Decomposition", *IEEE Intl. Conf. on Computer-Aided Design*, 1986, pp. 414-417.

[8] M. Garey and D. S. Johnson, *Computers and Intractability: A Guide to the Theory of NP-Completeness*, 1979.

[9] G. Golub and C. Van Loan, *Matrix Computations*, Baltimore, Johns Hopkins University Press, 1983.

[10] L. Hagen and A. B. Kahng, "Fast Spectral Methods for Ratio Cut Partitioning and Clustering", to appear in *Proc. IEEE Intl. Conf. on Computer-Aided Design*, Santa Clara, November 1991.

[11] K. M. Hall, "An r-dimensional Quadratic Placement Algorithm", *Management Science* 17(1970), pp. 219-229.

[12] M. Hanan, P. K. Wolff and B. J. Agule, "A Study of Placement Techniques", *J. Design Automation and Fault-Tolerant Computing* 2 (1978), pp. 28-61.

[13] S. Kang, "Linear Ordering and Application to Placement", *Proc. ACM/IEEE Design Automation Conf.* 1983, pp. 457-464.

[14] F. Kurdahi and S. Sastry, "On the Applicability of the Weibull Model to Characterize Wire Length Distributions in Standard Cell Layouts", *Proc. European Design Automation Conf.*, 1991.

[15] T. Lengauer, *Combinatorial Algorithms for Integrated Circuit Layout*, Wiley-Teubner, 1990.

[16] D.G. Schweikert and B.W. Kernighan, "A Proper Model for the Partitioning of Electrical Circuits", *ACM/IEEE Design Automation Conf.*, 1972.

[17] D. M. Schuler and E. G. Ulrich, "Clustering and Linear Placement", *IEEE Design Automation Workshop*, 1972, pp. 50-56.

[18] G. Sigl, K. Doll and F. M. Johannes, "Analytical Placement: A Linear or a Quadratic Objective Function?", *Proc. ACM/IEEE Design Automation Conf.*, June 1991, pp. 427-432.

[19] L. Steinberg, "The Backboard Wiring Problem: A Placement Algorithm", *SIAM Review* 3(1) (1961), pp. 37-50.

[20] R.S. Tsay, E.S. Kuh and C. Hsu, "PROUD: A Fast Sea-of-Gates Placement Algorithm", *Proc. ACM/IEEE Design Automation Conference*, 1988, pp. 318-323.

[21] Y.C. Wei and C.K. Cheng, "Ratio Cut Partitioning for Hierarchical Designs", *IEEE Trans. on CAD* 10(7) (1991), pp. 911-921.

[22] B. X. Weis and D. A. Mlynski, "A Graphtheoretic Approach to the Relative Placement Problem", *IEEE Trans. on CAS* (1988), pp. 286-293.