ON LINE SCHEME FOR COMPUTING ROTATION FACTORS

Milos D. Ercegovac Tomas Lang September 1986 CSD-860031

ON LINE SCHEME FOR COMPUTING ROTATION FACTORS

Milos D. Ercegovac `Tomas Lang

September 1986 CSD-860031

On-Line Scheme for Computing Rotation Factors

M. D. Ercegovac and T. Lang

UCLA Computer Science Department University of California Los Angeles, CA 90024

Abstract

An integrated radix-2 on-line algorithm for computing rotation factors for matrix transformations is presented. The inputs and outputs are in parallel form, conventional 2's complement, floating-point representation. The exponents are computed using conventional arithmetic while the significands are processed using on-line schemes. The conventional result is obtained by using an on-the-fly conversion scheme. The rotation factors are computed in 10+n clock cycles for n-bit significands. The clock period is kept small by the use of carry-save adder schemes. The implementation and performance of the algorithm are discussed.

1. Introduction

The rotation factors that we propose to compute are an essential part of matrix triangularization methods [GOL83] and the QR-decomposition [LUK86]. In particular, in systolic arrays in which the rotations are done in parallel, the time of computation of the rotation factors is critical since it determines the step time of the array. We present an implementation using on-line arithmetic [ERC84], since this approach is potentially advantageous in evaluating arithmetic expressions consisting of sequentially-dependent operations [ERC81]. The on-line scheme for the computation of rotation factors was proposed previously in [CIM81], where it is shown that it can produce a significant speed-up with respect to the use of conventional algorithms. However, the description is given at high-level and using previously developed algorithms for the operations of multiplication, division, and square root [TRI77, ERC78]. In this paper we develop an integrated algorithm for the rotation factors, which consists of modifications of the algorithms of

the primitive operations to provide for suitable interfaces with their predecessors and successors in the computation. In this way, both the on-line delay and the complexity of the implementation are reduced. The clock period is kept low by the use of carry-save adder techniques and the conversion of the result from redundant to conventional representation is done using an on-the-fly scheme [ERC85]. Moreover, the scheme is for floating-point representations.

We propose to compute the rotation factors of the Givens matrix transformation [GOL83], defined as

$$c = \frac{x}{(x^2 + y^2)^{1/2}} \tag{1.1}$$

and

$$s = \frac{y}{(x^2 + y^2)^{1/2}}$$

We assume that $x = fx \cdot 2^{ex}$, $y = fy \cdot 2^{ey} \ge 0$ (due to the characteristics of the transformation [GOL83]), $c = fc \cdot 2^{ec}$ and $s = fs \cdot 2^{es}$ are normalized floating-point numbers with *n*-bit fractions in two's complement bit-parallel form. The exponents are represented and processed in a conventional, bit-parallel manner and on-line representations and algorithms are used internally to compute the fractions.

The scheme, shown in Figure 1, performs the following functions:

- 1. Alignment of operands x and y
- 2. Computation of $z = x^2 + y^2$
- 3. Computation of $d = z^{1/2}$

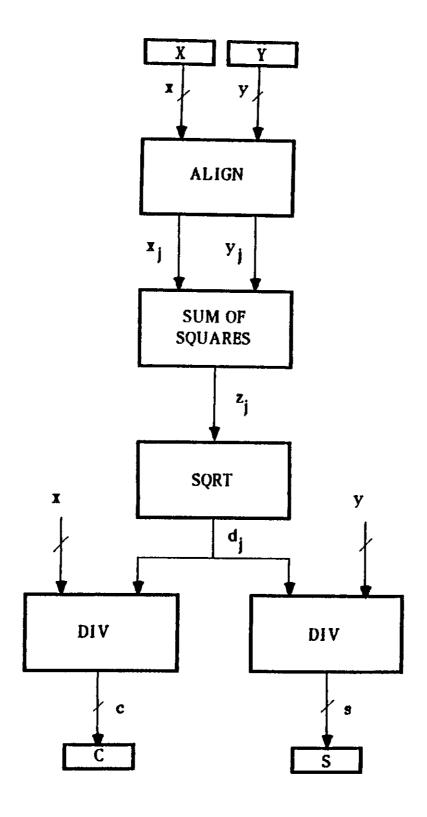


Figure 1: General Scheme

4. Computation of c = x/d and s = y/d

We now describe each component separately and then comment on the whole system.

2. Alignment

The alignment of the input operands is performed in on-line manner during the parallel-to-serial conversion according to following algorithm:

Algorithm Align

```
/* Get exponent difference and set common exponent */
ed = ex - ey; e = max(ex, ey);
x_0 = sign(x); y_0 = 0; /*(since y > 0)*/

/* Delay operand with smaller exponent */
if ed \ge 0 then

for i = 1, 2, ..., n

\{x_i = fx_i;

if i \le ed then y_i = 0;

else y_i = fy_{i-w}; }

else

for i = 1, 2, ..., n

\{y_i = fy_i;

if i \le |ed| then x_i = sign(x);

else x_i = fx_{i-|ed|}; }
```

end Align

The alignment scheme is shown in Figure 2. The exponent difference is obtained in one clock cycle. Thereafter, the bits of the aligned operands are obtained one per cycle. Note that the on-line delay $p_{align} = 0$.

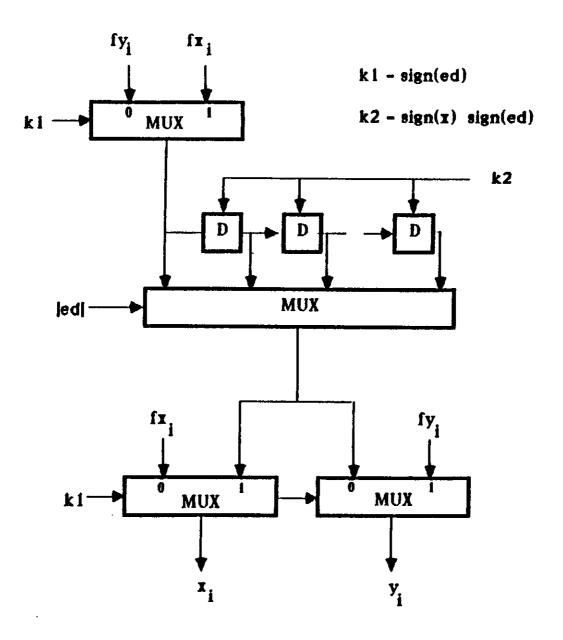


Figure 2: Alignment Scheme

3. On-line Sum of Squares Algorithm

In this section we present the algorithm and implementation for on-line sum of squares to be used in the computation of the rotation factors. The algorithm computes the fraction and the exponent of the result. The exponent is computed in a conventional, bit-parallel manner (see Section 6). Consequently, the following discussion deals with the computation of the fraction.

To incorporate a possible on-line delay we compute

$$z^* = 2^{-p}z = 2^{-p}(x^2 + y^2) \tag{3.1}$$

If the on-line forms of x, y, and z^* are

$$X[j] = X[j-1] + x_j 2^{-j}, \quad X[0] = -x_0 \quad (since \ x \ is \ in \ 2's \ complement)$$

$$Y[j] = Y[j-1] + y_j 2^{-j}, \quad Y[0] = 0 \quad (since \ y > 0)$$

$$Z[j] = Z[j-1] + z_j 2^{-j}, \quad Z[0] = 0$$

$$(3.2)$$

then we can define the residual function

$$w[j] = 2^{j} (2^{-p} X[j]^{2} + 2^{-p} Y[j]^{2} - Z[j])$$
(3.3)

From this residual expression we get the recurrence

$$w[j] = 2w[j-1] - z_j + (2X[j-1]x_j + x_j^2 2^{-j} + 2Y[j-1]y_j + y_j^2 2^{-j})2^{-p}$$
(3.5)

with the initial condition

$$w[0] = (X[0]^2 + Y[0]^2)2^{-p} = x_0 2^{-p}$$
(3.6)

To keep w[j] bounded we make the selection

$$z_{j} = integer(2w[j-1])$$
(3.7)

which transforms the recurrence into

$$w[j] = fraction(2w[j-1]) + (2X[j-1]x_j + x_j^2 2^{-j} + 2Y[j-1]y_j + y_j^2 2^{-j})2^{-p}$$
(3.8)

Since we want to implement this with a carry-save adder, the exact integer and fraction parts cannot be determined (without propagating carries). Consequently, the previous expressions are transformed into

$$z_j = csint(2w[j-1]) \tag{3.9}$$

$$w[j] = csfrac(2w[j-1]) + (2X[j-1]x_j + x_j^2 2^{-j} + 2Y[j-1]y_j + y_j^2 2^{-j})2^{-p}$$
(3.10)

Since $x_i = 0$ and $y_i = 0$ for i > n, we have

$$w[n+1+p] < 1$$

so that

$$|(x^2 + y^2) - Z[n+1+p]| < 2^{-n}$$

The corresponding carry-save operation is shown in Figure 3. From it we see that the bounds on z_i are (for |x|, |y| < 1 and $y \ge 0$)

$$-4 \le z_j \le 10 \text{ if } p=0$$
 $-2 \le z_j \le 6 \text{ if } p=1$
(3.11)

These values of z_j are over-redundant (that is, they are larger than 1 for a radix-2 representation). We choose this alternative because it simplifies the selection, reduces the on-line delay, and is suitable for the interface with the square root. We choose to implement the case with p=1 to minimize the overall on-line delay.

Figure 3: CSA Operation in Sum of Squares

The algorithm is summarized next:

Algorithm Sum of Squares

```
/* Initialization */
w[0] \leftarrow x_0 \times 2^{-1}; z_0 \leftarrow 0;
X[0] \leftarrow -x_0; Y[0] \leftarrow 0;

/* Recurrence */
for j = 1, 2, ..., n+1
\{ w[j] \leftarrow csfract(2w[j-1]) + (2X[j-1] + x_j 2^{-j})x_j + (2Y[j-1] + y_j 2^{-j})y_j;
z_j \leftarrow csint(2w[j-1]);
X[j] \leftarrow concat(X[j-1], x_j);
Y[j] \leftarrow concat(Y[j-1], y_j);
```

end Sum of Squares

The exponent of the result is 2e and the fraction is in the range $0.25 \le z^2 \le 2$.

Implementation

The scheme is shown in Figure 4. Note that its implementation complexity is similar to that of the on-line multiplier. The on-line delay is $p_{sum_of_squares} = 1$. The critical path consists of two multiplexers, one 4-to-2 carry-save adder, and a 4-bit CPA. The internal arithmetic is in 2's complement.

4. On-Line Square Root

We now describe an on-line square-root algorithm to interface with the sum of squares and the division, for the computation of rotation factors. This algorithm allows an over-redundant input digit set, differing in this respect from previous on-line square root algorithms [ERC78, OKL82].

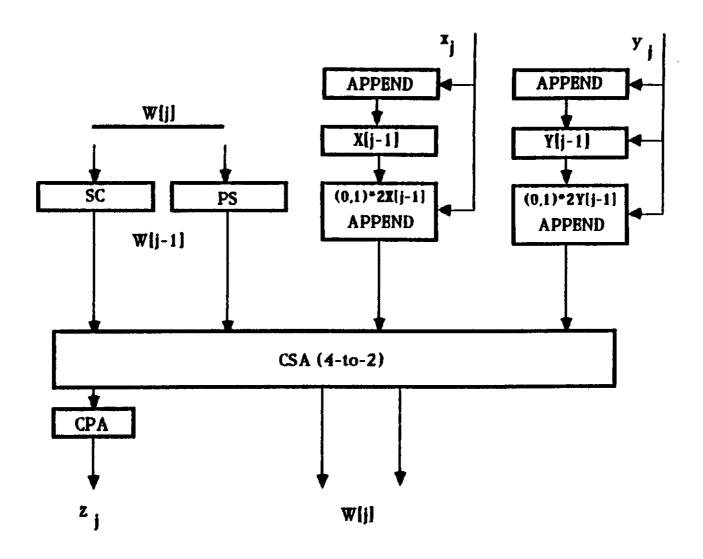


Figure 4: Sum of Squares Scheme

Let the on-line forms of the argument z and of the square root d be

$$Z[i] = Z[i-1] + z_i 2^{-i}, \quad -b \le z_i \le a$$
(4.1)

(where, according to the sum of squares implementation, b=2 and a=6)

$$D[i] = D[i-1] + d_i 2^{-i}, d_i \in \{-1,0,1\}$$

To bound the error of the computation we make

$$(D[i]-2^{-i})^2 + b2^{-p-i} \le Z[i+p] \le (D[i]+2^{-i})^2 - a2^{-p-i}$$
(4.2)

where p is the on-line delay to be determined later and, as indicated, the argument digits satisfy $z_i \in \{-b, ..., 0, ..., a\}$.

Consequently,

$$(-D[i]2^{-i+1} + 2^{-2i} + b2^{-p-i}) \le Z[i+p] - D[i]^2 \le (D[i]2^{-i+1} + 2^{-2i} - a2^{-p-i})$$
(4.3)

We can, therefore, define a residual R[i] such that

$$R[i] = 2^{i}(Z[i+p] - D[i]^{2}) \qquad R[0] = D[p]$$
(4.4)

satisfying the bounds

$$C_{-1}[i] = (-2D[i] + 2^{-i} + b2^{-p}) \le R[i] \le (2D[i] + 2^{-i} - a2^{-p}) = C_1[i]$$

$$(4.5)$$

The resulting recurrence is

$$R[i] = 2R[i-1] + z_{i+p}2^{-p} - 2d_iD[i-1] - d_i^2 2^{-i}$$
(4.6)

We now determine a selection function for d_i so that the bounds are satisfied. For this, we compute the intervals $[L_k, U_k]$ of R[i-1] so that the value $d_i=k$ (k=-1,0,1) can be selected and R[i] is inside the allowed interval of bounds defined by (4.5). The expression for R[i-1] is

$$R[i-1] = 2^{-1}R[i] - z_{i+p}2^{-p-1} + d_iD[i-1] + d_i^22^{-i-1}$$
(4.7)

The intervals are

For $d_i = 1$,

$$\begin{split} L_1 &= -D\left[i\right] + 2^{-i-1} + b \, 2^{-p-1} - z_{i+p} 2^{-p-1} + D\left[i-1\right] + 2^{-i-1} = b \, 2^{-p-1} - z_{i+p} 2^{-p-1} \\ U_1 &= D\left[i\right] + 2^{-i-1} - a \, 2^{-p-1} - z_{i+p} 2^{-p-1} + D\left[i-1\right] + 2^{-i-1} \\ &= 2D\left[i-1\right] + 2^{-i+1} - a \, 2^{-p-1} - z_{i+p} 2^{-p-1} \end{split}$$

For $d_i = 0$,

$$L_0 = -D[i] + 2^{-i-1} + b 2^{-p-1} - z_{i+p} 2^{-p-1} = -D[i-1] + 2^{-i-1} + b 2^{-p-1} - z_{i+p} 2^{-p-1}$$

$$U_0 = D[i-1] + 2^{-i-1} - a 2^{-p-1} - z_{i+p} 2^{-p-1}$$

and for $d_i = -1$,

$$L_{-1} = -D[i] + 2^{-i-1} + b 2^{-p-1} - z_{i+p} 2^{-p-1} - D[i-1] + 2^{-i-1}$$

$$= -2D[i-1] + 2^{-i+1} + b 2^{-p-1} - z_{i+p} 2^{-p-1}$$

$$U_{-1} = D[i] + 2^{-i-1} - a 2^{-p-1} - z_{i+p} 2^{-p-1} - D[i-1] + 2^{-i-1} = -a 2^{-p-1} - z_{i+p} 2^{-p-1}$$
(4.10)

Since $(-z_{i+p}2^{-p-1})$ appears in all the expressions, we will base the selection on

$$R^*[i-1] = R[i-1] + z_{i+p} 2^{-p-1}$$
(4.11)

Consequently,

$$L_{1}^{*} = b 2^{-p-1}$$

$$U_{1}^{*} = 2D [i-1] + 2_{-i+1} - a 2^{-p-1}$$

$$L_{0}^{*} = -D [i-1] + 2^{-i-1} + b 2^{-p-1}$$
(4.12)

$$U_0^* = D[i-1] + 2^{-i-1} - a 2^{-p-1}$$

$$L_{-1}^* = -2D[i-1] + 2^{-i+1} + b 2^{-p-1}$$

$$U_{-1}^* = -a 2^{-p-1}$$

The containment conditions, $U_1 \le C_1[i-1]$ and $L_{-1} \le C_{-1}[i-1]$, require that

$$-b \le z_i \le a \tag{4.13}$$

which is satisfied by selecting a and b according to (3.11).

The continuity of the selection intervals and the use of carry-save adders require that the interval overlaps satisfy the following conditions:

$$\Delta_{01} = U_0^* - L_1^* = D[i-1] + 2^{-i-1} - (a+b)2^{-p-1} \ge 2^{-t}$$

$$\Delta_{10}^- = U_{-1}^* - L_0^* = D[i-1] - 2^{-i-1} - (a+b)2^{-p-1} \ge 2^{-t}$$
(4.14)

where t is the precision of the assimilated remainder.

Since $z \ge 2^{-2}$ (from the sum of squares), the smallest possible value of $d=2^{-1}$. Therefore, for a=6 and b=2 (as produced by the sum of squares), we get

$$\Delta_{01} \min = 2^{-1} - 4 \cdot 2^{-p} \ge 2^{-t}$$

$$\Delta_{10}^{-} \min = 2^{-1} - 2^{-i-1} - 4 \cdot 2^{-p} \ge 2^{-t}$$
(4.15)

Since $d \ge 1/2$, the first negative digit cannot happen for i < 3. Therefore, the positive overlaps imply $p \ge 4$ and $t \ge 2$.

We now determine the selection constants k_{01} and k_{10}^- , assuming p=4, a=6 and b=2. The overlap region for the selection of 0 or 1 is bounded by

$$L_1^*(max) = b2^{-p-1} = 2^{-4}$$
 (4.16)

$$U_0^*(min) = D[i-1] + 2^{-i-1} - a2^{-p-1} > 2^{-1} - 6\cdot 2^{-5} = 5/16$$

Since the error in the remainder estimate is always positive (because of the use of carry-save adders and the 2's complement representation), we can choose $k_{01} = 1/16$. For this choice we require $t \ge 2$.

The overlap region for choosing between 0 and -1 is

$$L_0^*(max) = -D[i-1] + 2^{-i-1} + b 2^{-p-1} \le -2^{-1} + 2^{-3-1} + 2^{-4} = -3/8$$

$$U_{-1}^*(min) = -a 2^{-p-1} = -3/16$$
(4.17)

which leads to choice of $k_{10}^- = -3/8$ with $t \ge 3$.

Taking into account both cases, a suitable choice is

$$k_{01} = 1/8$$
 and $k_{10} = -3/8$ with $t \ge 3$

The corresponding algorithm for on-line square root is summarized next.

Algorithm Sqrt

/* Initialization */

$$R [-4] \leftarrow 0; Z[0] \leftarrow 0; D[0] \leftarrow 0; d_0 \leftarrow 0;$$

/* Accumulate 4 input digits */

for i=-3,-2,-1,0

 $\{R[i] \leftarrow 2R[i-1] + z_{4+i}2^{-4};\}$

/*Recurrence*/

for i=1,2,...,n+4

 $\{R^*[i] = \hat{R}[i-1] + z_{4+i}2^{-5};$

$$d_i = \begin{cases} 1 & \text{if } \hat{R}^*[i] > 1/8 \\ 0 & \text{if } 1/8 \ge \hat{R}^*[i] \ge -3/8 \\ -1 & \text{if } \hat{R}^*[i] < -3/8 \end{cases}$$

$$R[i] \leftarrow 2R[i-1] + z_{4+i}2^{-4} - 2d_iD[i-1] - d_i^22^{-i};$$

 $D[i] \leftarrow (D[i-1],d_i);$

end Sqrt

The result significand is in the range $[1/2,2^{1/2}]$ and the exponent is e.

Implementation

The square root scheme is shown in Figure 5. The internal arithmetic is performed in 2's complement system. The signed-digit operands are converted to 2's complement using the onthe-fly conversion method [ERC85]. To avoid costly sign-extension to subtract $d_i^2 2^{-i}$, we assume a borrow-save subtractor. The critical path, shown in Figure 6, consists of a 5-bit CPA, one multiplexers, and a 3-to-2 borrow-save subtractor.

5. On-line Division Algorithm and Implementation

We now present the algorithm for the division operation. We consider the computation of s (that of c is similar).

To incorporate the necessary on-line delay p we redefine the division operation as

$$s = 2^{-p} \frac{y}{d} \tag{5.1}$$

where s is obtained without delay. The real quotient is obtained by shifting s by p positions, that is eliminating the leading 0's. Consequently, the real quotient is obtained with an on-line delay of p.

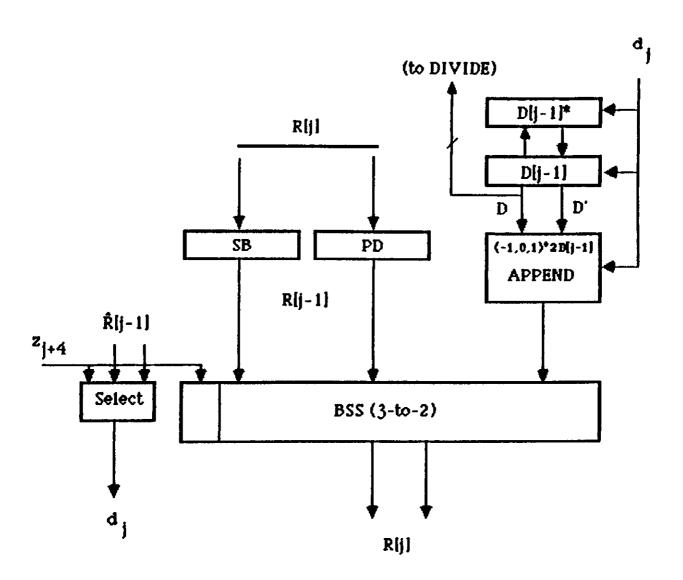


Figure 5: Square Root Scheme

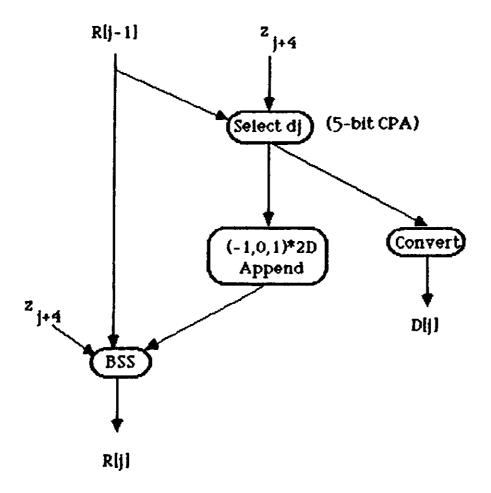


Figure 6: Critical Paths in Square Root Scheme

Since we are developing an algorithm that is specifically suited for the computation of the rotation factors, we assume that y (the dividend) is known in parallel form (not on-line), while d is on-line and s is obtained also on-line (but converted on-the-fly to conventional representation). This formulation leads to the following recurrence. Let

$$P[j] = 2^{j}(2^{-p}y - S[j] \times D[j])$$
(5.2)

be a patial remainder, satisfying the bound by

The corresponding recurrence is

$$P[j] = 2P[j-1] + S[j]d_i - D[j]s_i$$
(5.4)

with

$$P[0] = 2^{-p}y ag{5.5}$$

To determine the actual bounds on P[j] we calculate the interval $[L_k, U_k]$ of 2P[j-1] such that $s_j=k$ is a valid choice. From the recurrence we get

$$L_k - 2^{-p} - kD[j] \le P[j](k) \le U_k + 2^{-p} - kD[j]$$
(5.6)

since $|S[j]| \le 2^{-p}$.

To keep the remainder bounded we make

$$c_{-1} = \frac{L_{-1}}{2} \le P[j] \le \frac{U_1}{2} = c_1 \tag{5.7}$$

Consequently,

$$U_1 = 2D[j] - 2^{-p+1}$$
 (5.8)

$$L_{-1} = -2D[j] + 2^{-p+1}$$
(5.9)

Therefore,

$$c_1 = -c_{-1} = c = D[j] - 2^{-p}$$
(5.10)

Computation of p and t

Since we want to perform the additions/subtractions in carry-save form and the quotient selections using a limited-precision remainder (with precision of 2^{-l}), it is necessary that the selection regions for k and k-1 overlap by at least 2^{-l} . We base the selection on

$$W[j] = 2P[j-1] - S[j-1]d_j = P[j] + D[j]s_j$$
(5.11)

The selection interval for $s_i = k$ is

$$A_k = -c + kD_i \le W_i \le c + kD_i = B_k \tag{5.12}$$

and the overlap is

$$\Delta(k,k-1) = B_{k-1} - A_k = c + (k-1)D[j] + c - kD[j] = 2c - D[j]$$
(5.13)

Replacing the value of c, we get

$$D[j] - 2^{-p+1} \ge 2^{-t} \tag{5.14}$$

so that

$$2^{-p} \le \frac{D[j] - 2^{-t}}{2} \tag{5.15}$$

For $D[j] \ge 1/2$ we get

$$2^{-p} \le \frac{1 - 2^{-t+1}}{4} \tag{5.15}$$

A possible solution to this inequality is

$$p \ge 3$$
 and $t \ge 2$ (5.16)

Selection Constants

We now determine suitable selection constants. For p = 3, we have the following intervals:

for
$$s_j = -1 : [-2D[j] + 2^{-3}, -2^{-3}]$$

for
$$s_j = 0 : [-D[j] + 2^{-3}, D[j] - 2^{-3})$$

for
$$s_j = 1 : [2^{-3}, 2D[j] - 2^{-3})$$

To have a selection that is independent of the value of D[j] we look at the values of D[j] that produce the smallest intervals. This occurs in all cases for D[j] = 1/2. The resulting intervals are:

for
$$s_i = -1 : [-7/8, -1/8)$$

for
$$s_j = 0 : [-3/8, +3/8)$$

for
$$s_j = 1 : [+1/8, +7/8]$$

Since in 2's complement we have

$$\hat{R} = R - \varepsilon \tag{5.18}$$

with $0 \le \varepsilon \le 2^{-t}$, we get as selection constants

for t=2: +1/8 and -3/8

for t=3: +1/4 and -1/4.

The corresponding algorithm (using t=2) is given next.

Algorithm Division

/* Initialization */
$$P[0] \leftarrow y \times 2^{-3}; \ D[0] \leftarrow 0; \ S[0] \leftarrow 0; \ s_0 \leftarrow 0;$$

/* Recurrence */
for j=1,2,...,n+3:

$$\{W[j] = 2P[j-1] - S[j-1]d_j; \\ P[j] \leftarrow W[j] - D[j]s_j;$$

/* Note that $S[j-1] < 2^{-p} */$

$$\hat{W}[j] = (2P[j-1])_4 - (1-sign(d_j));$$

$$\begin{cases} -1 \ \hat{W}[j] \le -3/8 \\ 0 \ -3/8 < \hat{W}[j] < 1/8 \\ +1 \ \hat{W}[j] \ge 1/8 \end{cases}$$

$$D[j] \leftarrow D[j-1] + d_j 2^{-j};$$

$$S[j] \leftarrow S[j-1] + s_j 2^{-j};$$

end Divide

 $(A)_4$ means the four most significant bits of A.

Implementation

The implementation is shown in Figure 7. Note that the additions/subtractions are done using carry-save adders in 2's complement system; for this reason, the divisor and the quotient are converted on-the-fly [ERC85] into conventional form to be used as operands for the additions.

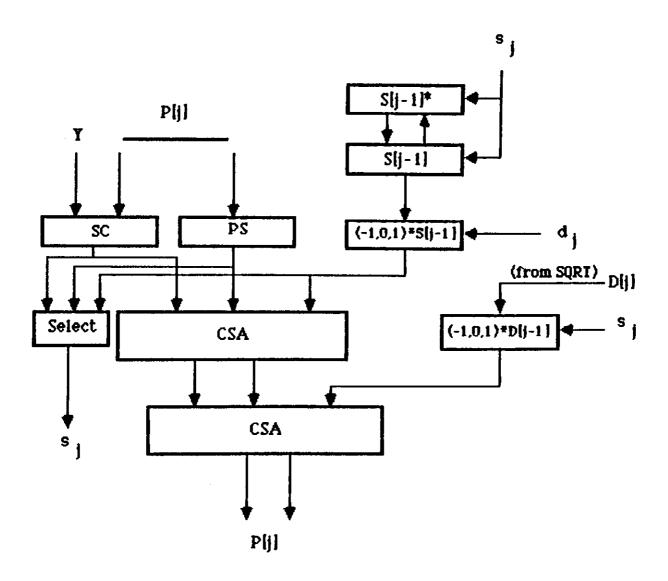


Figure 7: Division Scheme

The critical path is shown in Figure 8. We estimate that its delay corresponds to two multiplexers, one 4-bit CPA and one CSA.

6. Processing of Exponents

The exponents of the result can be obtained directly, instead of going through the intermediate steps. Since the sequence is sum of squares, square root, and division, we get

$$ec = ex - e$$
 $es = ey - e$
where $e = max(ex, ey)$. Therefore, if $ed = ex - ey$ we get

$$ec = \begin{cases} 0 & \text{if } ed \ge 0 \\ ed & \text{otherwise} \end{cases}$$

$$es = \begin{cases} -ed & \text{if } ed \ge 0\\ 0 & \text{otherwise} \end{cases}$$

7. Summary

An integrated scheme for computing rotation factors using on-line arithmetic algorithms has been presented. The scheme has the following characteristics:

- The inputs and outputs are in a conventional floating-point, bit-parallel format, 2's complement;
 - The latency of the scheme is 10+n cycles for n-bit significands;
- The cycle time is determined roughly by one carry-save adder, two multiplexers, and a 5-bit carry-propagate adder.

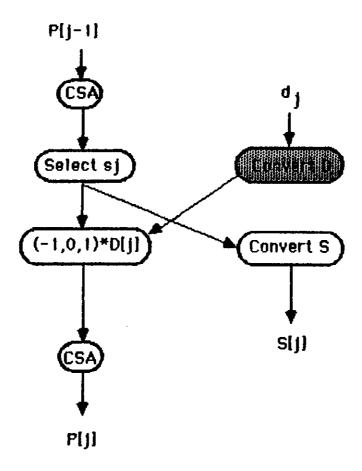


Figure 8: Critical Peths in Division Scheme

- The on-line algorithms operate internally on 2's complement representations so that efficient carry-save and borrow-save structures can be used.

Acknowledgements This research has been supported in part by the ONR Contract N00014-85-K-0159 "On-Line Arithmetic Algorithms and Structures for VLSI". We also thank Dr. J. G. Nash of Hughes Research Laboratories, Malibu, for his interest and support.

References

[CIM81] L. Ciminiera, A. Serra, and A. Valenzano, "Fast and Accurate Matrix Triangularization Using an Iterative Structure", *Proc. 5th IEEE Symposium on Computer Arithmetic*, Ann Arbor, 1981, pp. 215-221.

[ERC78] M.D. Ercegovac, "An On-Line Square Root Algorithm", Proc. 4th IEEE Symposium on Computer Arithmetic, Santa Monica, 1978, pp.183-189.

[ERC81] M.D. Ercegovac and A. Grnarov, "On the Performance of On-Line Arithmetic", *Proc.* 1980 Intl. Conference on Parallel Processing, 1980, pp.55-62.

[ERC84] M.D. Ercegovac, "On-Line Arithmetic: An Overview", *Proc. SPIE 1984*, Vol.495 - Real Time Signal Processing VII, 1984, pp.86-93.

[ERC85] M.D. Ercegovac and T. Lang, "On-the-Fly Conversion of Redundant into Conventional Representations", UCLA Computer Science Department Technical Report No. CSD 850026, August 1985. (accepted for publication IEEE Transactions on Computers).

[GOL83] G.H. Golub and C. F. Van Loan, *Matrix Computations*, Baltimore: The Johns Hopkins University Press, 1983.

[LUK86] F. T. Luk, "Architectures for Computing Eigenvalues and SVDs", SPIE Proc. Highly Parallel Signal Processing Architectures, Vol 614, 1986, pp. 24-33.

[OKL82] V.G. Oklobdzija and M.D. Ercegovac, "An On-Line Square Root Algorithm", *IEEE Transactions on Computers*, Vol. C-31, No.1, January 1982, pp.70-75.

[TRI77] K.S. Trivedi and M.D. Ercegovac, "On-Line Algorithms for Division and Multiplication", *IEEE Transactions on Computers*, Vol. C-26, No.7, July 1977, pp.667-680.

,		